



**Manufacturer:** NI

**Board Assembly Part Numbers** (Refer to Procedure 1 for identification procedure):

| Part Number and Revision | Description        |
|--------------------------|--------------------|
| 148882B-01L or later     | PXIe-5841 VST      |
| 148882F-03L or later     | PXIe-5841 VSG      |
| 148882F-05L or later     | PX PXIe-5841 VSA   |
| 148882C-04L or later     | AST-1000 6 GHZ VST |

**Volatile Memory**

| Target Data               | Type  | Size                      | Battery Backup | User <sup>1</sup> Accessible | System Accessible | Sanitization Procedure |
|---------------------------|-------|---------------------------|----------------|------------------------------|-------------------|------------------------|
| Waveform storage – Bank 1 | DRAM  | 2 GB                      | No             | Yes                          | Yes               | Cycle Power            |
| Waveform storage – Bank 2 | DRAM  | 2 GB                      | No             | Yes                          | Yes               | Cycle Power            |
| FPGA Image Memory         | SDRAM | 64 MB                     | No             | Yes                          | Yes               | Cycle Power            |
| Device Operation          | FPGA  | Xilinx<br>XC7VX690T       | No             | Yes                          | Yes               | Cycle Power            |
| Device Operation          | FPGA  | Intel<br>10M16SCU         | No             | No                           | Yes               | Cycle Power            |
| Device Operation          | FPGA  | Intel<br>EPM1270G         | No             | No                           | Yes               | Cycle Power            |
| Device Operation (x2)     | CPLD  | Intel<br>10M08SAU<br>(x2) | No             | No                           | Yes               | Cycle Power            |
| Device Operation Storage  | SRAM  | 2 MB                      | No             | Yes                          | Yes               | Cycle Power            |

**Non-Volatile Memory (incl. Media Storage)**

| Target Data   | Type  | Size              | Battery Backup | User Accessible | System Accessible | Sanitization Procedure |
|---|-------|-------------------|----------------|-----------------|-------------------|------------------------|
| Device configuration  | Flash | 256 MB            | No             |                 |                   |                        |
| <ul style="list-style-type: none"> <li>• Device configuration and info</li> <li>• FPGA bitstream</li> <li>• Calibration metadata</li> <li>• Calibration data<sup>2</sup></li> </ul> |       |                   |                | No              | Yes               | None                   |
|   |       |                   |                | No              | Yes               | None                   |
|   |       |                   |                | Yes             | Yes               | Procedure 2            |
|   |       |                   |                | No              | Yes               | None                   |
| Device Operation  | FPGA  | Intel<br>EPM1270G | No             | No              | Yes               | None                   |
| Device Operation  | FPGA  | Intel<br>10M16SCU | No             | No              | Yes               | None                   |

<sup>1</sup> Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

<sup>2</sup> Calibration constants that are stored on the device include information for the device’s full operating range. Any implications resulting from partial self-calibration can be eliminated by running the full self-calibration procedure.



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|                       |      |                           |    |    |     |      |
|-----------------------|------|---------------------------|----|----|-----|------|
| Device Operation (x2) | CPLD | Intel<br>10M08SAU<br>(x2) | No | No | Yes | None |
|-----------------------|------|---------------------------|----|----|-----|------|

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## Procedures

### **Procedure 1 – Board Assembly Part Number identification:**

To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “P/N: 148882#-xxL” where “#” is the letter revision of the assembly and “xx” describes the product variant.

### **Procedure 2 - Device Configuration Flash (Calibration Metadata):**

The user-accessible portion of the Device Configuration Flash is limited to a programmable calibration password, which can be programmed through the NI-RFSG driver in the LabVIEW application. To clear the calibration password, complete the following steps in an empty LabVIEW VI and run:

1. Open an NI-RFSG session using the niRFSG Initialize VI.
2. To clear/reset the calibration password:
  - a. Add the niRFSG Change External Calibration Password VI after the niRFSG Initialize VI.
  - b. Specify the current password in the “password” input of the niRFSG Change External Calibration Password VI.
  - c. Wire a string of 32 “0” characters to the “new password” input of the niRFSG Change External Calibration Password VI.
3. Close the NI-RFSG session using the niRFSG Close VI.



## Terms and Definitions

### **Cycle Power:**

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

### **Volatile Memory:**

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

### **Non-Volatile Memory:**

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

### **User Accessible:**

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

### **System Accessible:**

The component is read and/or write addressable from the host without the need to physically alter the product.

### **Clearing:**

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

### **Sanitization:**

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.