Manufacturer: NI

Board Assembly Part Numbers (Refer to Procedure 1 for identification procedure)**:**

Part Number and Revision	Description
174638A-01L or later **174638A-02L or later**	**PXIe-5552**

Volatile Memory

Target Data	*Type*	*Size*	*Battery Backup*	*User[1] Accessible*	*System Accessible*	*Sanitization Procedure*
Device operation	FPGA	Xilinx XC6SLX45	No	Yes	Yes	Cycle Power
Device operation (x2)	FPGA	Intel 10M08SAU (x2)	No	Yes	Yes	Cycle Power

Non-Volatile Memory (*incl. Media Storage*)

Target Data	*Type*	*Size*	*Battery Backup*	*User Accessible*	*System Accessible*	*Sanitization Procedure*
Device configuration	Flash	64 MB	No			
• Device configuration and info				No	Yes	None
• Calibration metadata				Yes	Yes	Procedure 2
• Calibration data				No	Yes	None
Control FPGA (x2)	FPGA	Intel 10M08SAU (x2)	No			
• Internal flash memory				No	Yes	None
• Configure image				No	Yes	None

[1] Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

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Procedures

Procedure 1 – Board Assembly Part Number identification:
To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of the product. The Assembly Part Number will be formatted as “P/N: 174638a-0bL” where the “a” is the letter revision of the assembly (e.g. A, B, C...) and “b” is the model variant number.

Procedure 2 – Device Configuration Flash (Calibration Metadata):
Requirements: NI-VRTS driver version 24.8 or later that supports this model.

The user-accessible area of the Device Configuration Flash is limited to a calibration password, which can be written using the Measurement & Automation Explorer application installed by NI-VRTS. To clear the calibration password, complete the following steps.

1. Open the Measurement & Automation Explorer application and select the NI PXIe-5552 assembly in the left-hand panel.
2. In the right-hand panel, select Change Password.
3. Enter the current password in the Old Password field. Use the New Password and Confirm Password fields to set the new password to blank (empty string)

Terms and Definitions

Cycle Power:
The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:
Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:
Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:
The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:
The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:
Per *NIST Special Publication 800-88 Revision 1*, "clearing" is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:
Per *NIST Special Publication 800-88 Revision 1*, "sanitization" is a process to render access to "Target Data" on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.